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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/083,011	02/25/2002	Edouard Ritz	PF010024	4305

7590 08/24/2004
JOSEPH S. TRIPOLI
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EXAMINER

NATNAEL, PAULOS M

ART UNIT	PAPER NUMBER
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2614

DATE MAILED: 08/24/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/083,011

Applicant(s)

RITZ ET AL.

Examiner

Paulos M. Natnael

Art Unit

2614

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 2.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims **1-8** are rejected under 35 U.S.C. 102(e) as being anticipated by **Cottle, et al.**, U.S. Pat. No. 6,263,396.

Considering claim 1, the claimed video apparatus with a digital decoder having a first memory on an internal bus and linked to an OSD circuit and to a second memory via a main bus, the video apparatus comprising means for realising a DMA transfer between the first memory and the second memory, is met by Fig. **16A** in which is illustrated SDRAM 312DMA-transfer capable with or to 32-bit DATA RAM 240, through TC bus, MPEG Decoder 250 and OSD processor 270.

Considering claim 2, a video apparatus according to claim 1, wherein a CPU is connected to the main bus, is met by CP 280 (fig.16A, see also Fig. 1B).

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Considering claim 3, a video apparatus according to claim 2, wherein the second memory is used by the CPU, is met by Data RAM 240, (fig.1B, see also Fig. 16A)

Considering claim 4, a video apparatus according to claim 1 wherein the first memory is a Video RAM and wherein the second memory is a CPU RAM, are met by SDRAM 312 and data RAM 240, respectively (fig. 1B and 16A).

Considering claim 5, a video apparatus according to claim 1, wherein the digital decoder is connected to a digital front-end, is met by MPEG Decoder 250, Fig. 1B or 16A (see also Fig.1, 200 part of 100)

Considering claim 6, a process for controlling a video apparatus with a digital decoder having a first memory on an internal bus and linked to an OSD circuit and to a second memory via a main bus, comprising the step of realising a DMA transfer between the first memory and the second memory via the digital decoder.

Regarding claim 6, See rejection of claim 1;

Considering claim 7,

a) issuing a request for the OSD circuit to use more than a given size in the second memory, is inherent because the CPU controls the system and may request/command to do so. (see Request Fig.16C)

b) realising a DMA transfer from the second memory to the first memory.

Regarding b), see rejection of claim 1;

Considering claim 8, a process according to claim 7, with the further steps of :

a) issuing a request for the OSD circuit to use data in the first memory, is inherent because the CPU controls the system and may request/command to do so. (see Request Fig.16C)

b) copying via a DMA transfer data from the second memory to the first memory ;
realising a DMA transfer of the requested data from the first memory to the second memory;

See rejection of claim 1;

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

4. Muth, U.S. Pat. No. 6,774,918 discloses video overlay processor with reduced memory and bus performance requirements.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paulos M. Natnael whose telephone number is (703) 305-0019. The examiner can normally be reached on 9:00am - 5:30pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Miller can be reached on (703) 305-4795. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PMN
August 21, 2004


PAULOS M. NATNAEL
PATENT EXAMINER